

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
24 June 2004 (24.06.2004)

PCT

(10) International Publication Number
WO 2004/053683 A3

(51) International Patent Classification⁷: **G06F 1/00**

(21) International Application Number:
PCT/GB2003/004256

(22) International Filing Date: 6 October 2003 (06.10.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0229068.2 12 December 2002 (12.12.2002) GB
0302650.7 5 February 2003 (05.02.2003) GB
0302646.5 5 February 2003 (05.02.2003) GB
0307823.5 4 April 2003 (04.04.2003) GB

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(81) Designated States (national): GB, JP, US.

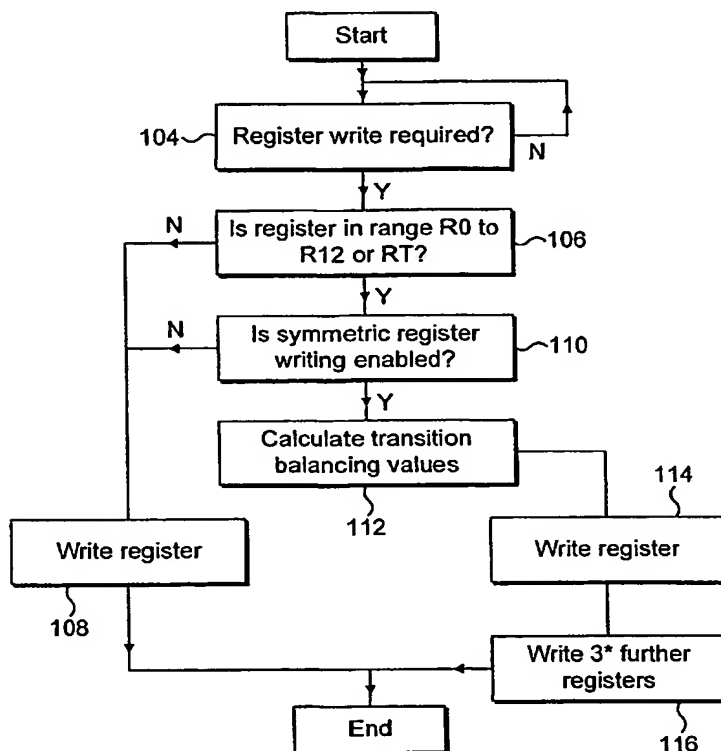
Published:

— with international search report

— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

[Continued on next page]

(54) Title: PROCESSING ACTIVITY MASKING IN A DATA PROCESSING SYSTEM



(57) Abstract: A data processing system is provided with mechanisms such that when a data value is stored within a data register, further data values are stored within one or more further registers such that the total number of signal transitions from high to low and from low to high does not vary in dependence upon the data value being written or the previous data value.



(88) Date of publication of the international search report:
5 August 2004

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INTERNATIONAL SEARCH REPORT

Application No

PCT/GB 03/04256

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G06F1/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F G07F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99/67766 A (CRYPTOGRAPHY RESEARCH INC ; KOCHER PAUL C (US); JAFFE JOSHUA M (US); J) 29 December 1999 (1999-12-29)	1,7
A	page 7, line 15 - line 18 page 7, line 25 - page 8, line 4 page 8, line 11 - line 13	2-6,8-12
A	ALMANEI S: "PROTECTING SMART CARDS FROM POWER ANALYSIS ATTACKS" 'Online! 28 May 2002 (2002-05-28), XP001130984 Retrieved from the Internet: URL: http://islab.oregonstate.edu/koc/ece679/project/2002/almanei.pdf 'retrieved on 2002-10-17! the whole document	1-12



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Date of the actual completion of the international search

25 May 2004

Date of mailing of the international search report

14/06/2004

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INTERNATIONAL SEARCH REPORT

Information on patent family members

Int. Application No
PCT/GB 03/04256

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